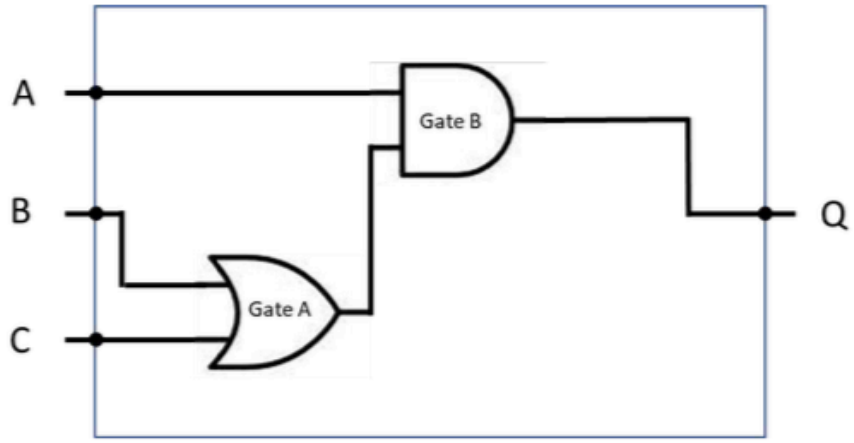


Logic Gate Question answers

Question 03.2 – the one you don't need to draw the gates on

Label	Gate
L1	NOT
L2	AND
L3	AND
L4	OR

Question 15 (the one you have to draw)

15	<p>3 marks for AO2 (apply)</p>  <pre> graph LR A((A)) --- GB[Gate B] B((B)) --- GA[Gate A] C((C)) --- GA GA --> GB GB --> Q((Q)) </pre> <ul style="list-style-type: none"> • OR Gate, with correct symbol used, with TWO inputs from B and C; • AND Gate, with correct symbol used, with TWO inputs from A and Gate A (even if Gate A is an incorrect gate); • Output from Gate B is the only connection to Q (even if Gate B is an incorrect gate); 	3
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